Inventors: Jerome J. CARTMELL, et al. Filing Date: March 29, 2004

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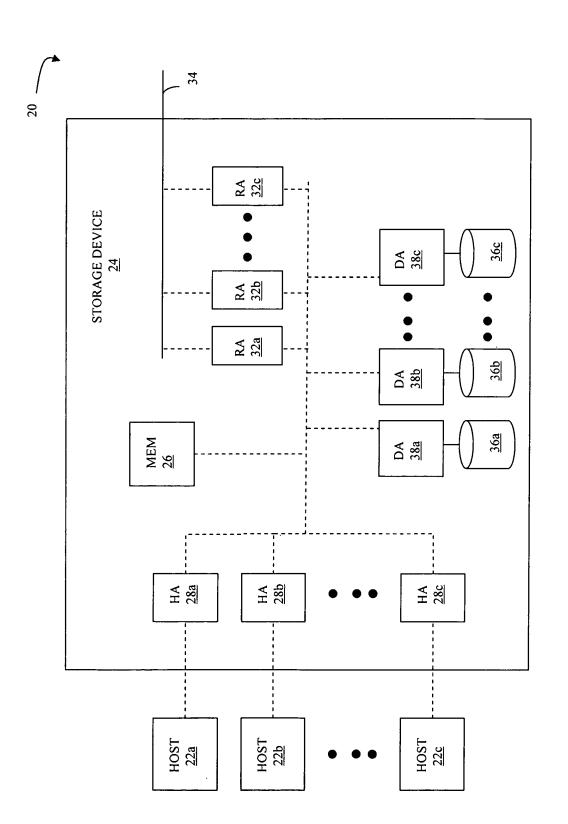


FIGURE 1

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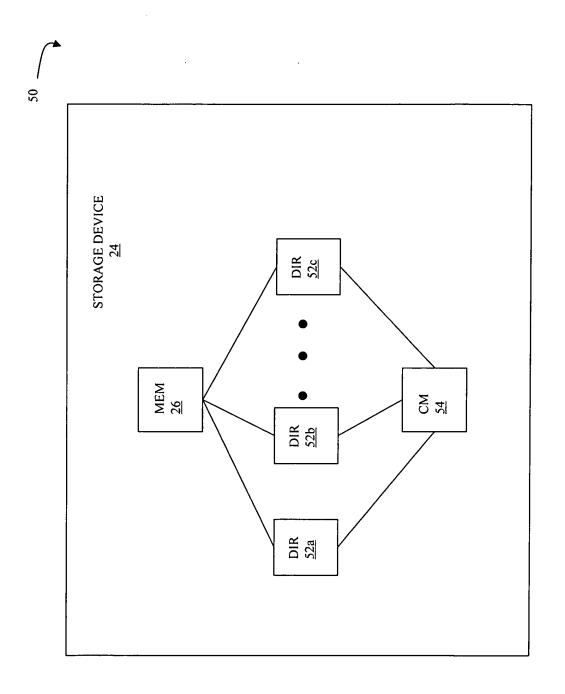
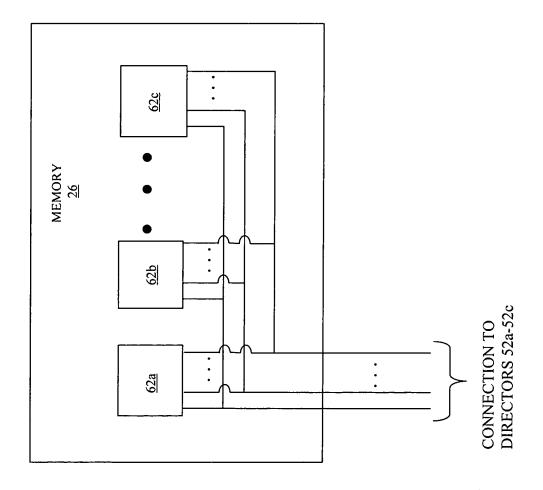


FIGURE 2

**MEMORY** 

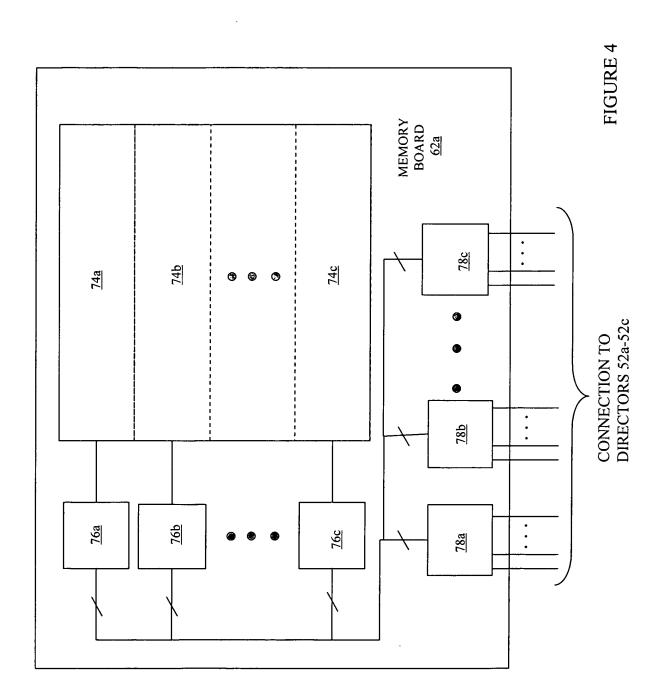
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FIGURE 3

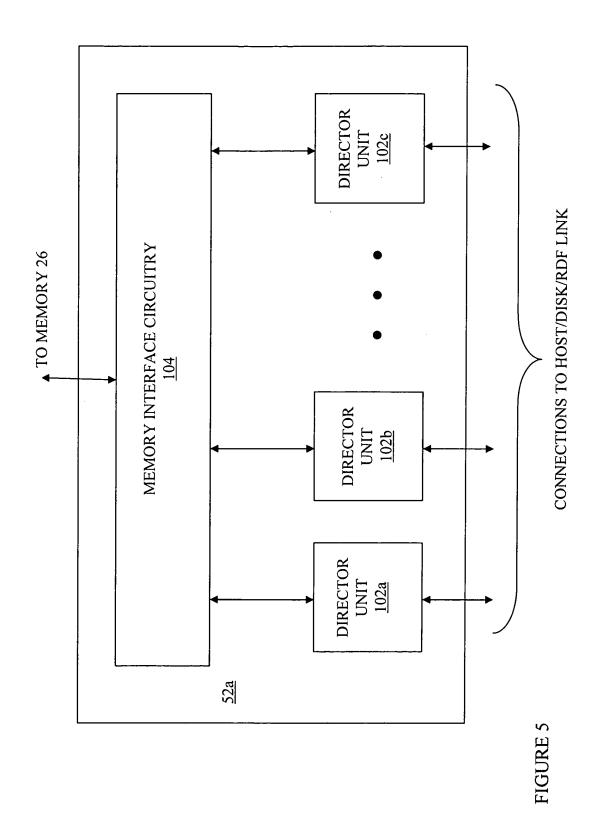


**MEMORY** 

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**MEMORY** 

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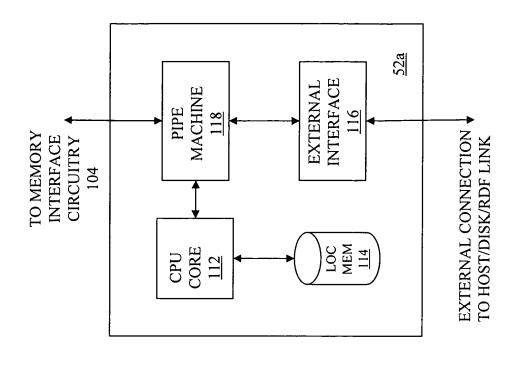


FIGURE 7

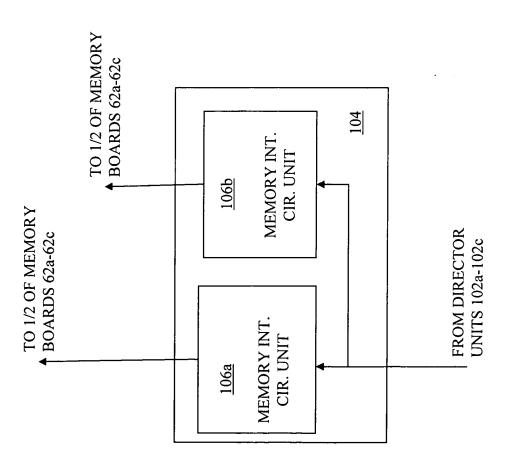


FIGURE 6

**MEMORY** 

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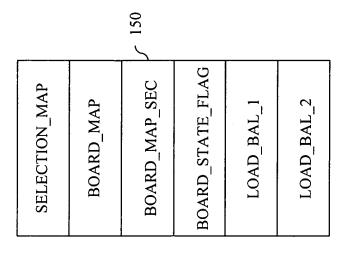


FIGURE 9

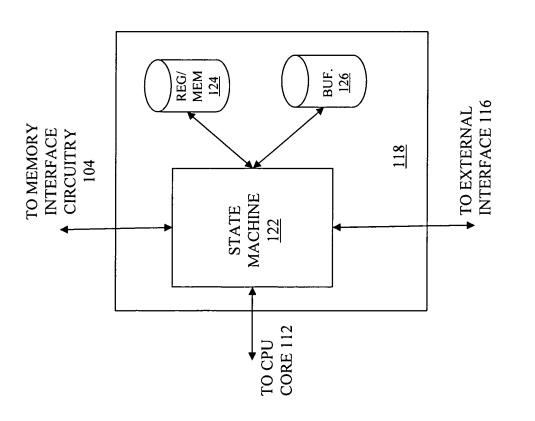
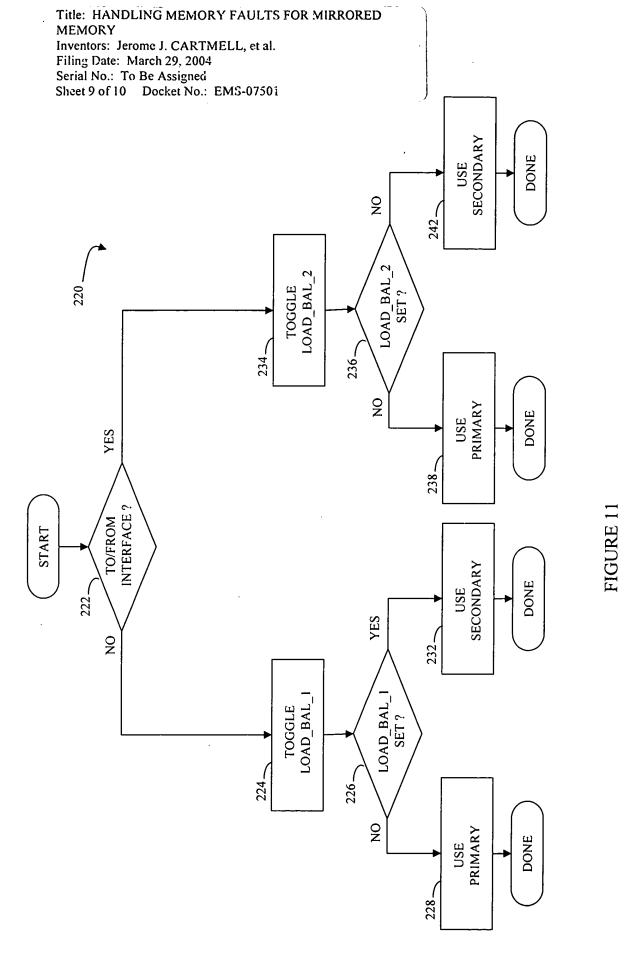


FIGURE 8

Title: HANDLING MEMORY FAULTS FOR MIRRORED **MEMORY** Inventors: Jerome J. CARTMELL, et al. Filing Date: March 29, 2004 Scrial No.: To Bc Assigned Sheet 8 of 10 Docket No.: EMS-07501 READ/WRITE USING PRIMARY DONE 184 YES WAIT **USING SECONDARY** READ/WRITE PRIM SEL? 9N DONE 174\_ 9 YES YES 188 SPEC MIRROR? WRITEABLE? FIGURE 10 BLOCKED? DUAL WRT MODE? **WRT AND** YES YES START <u>8</u> 176\_ 186\_ 172\_ 182\_ 9 WRITE TO BOTH 8 <u>N</u> READ? DONE RETURN ERROR DONE 194 <u> 1</u>961 YES 178 LOAD BALANCE ALGORITHM READ FROM PRIM OR SEC DONE 202 198



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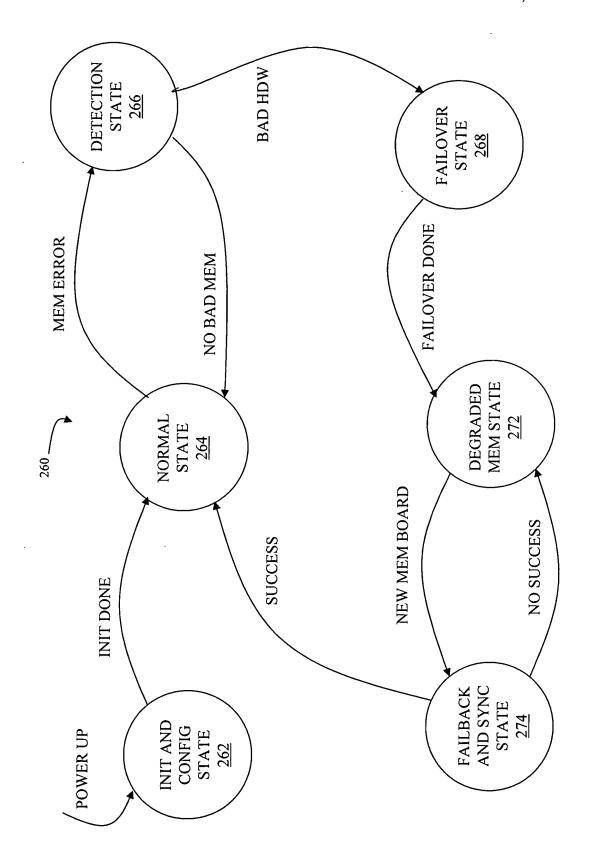


FIGURE 12